

FIG. 1  
(PRIOR ART)

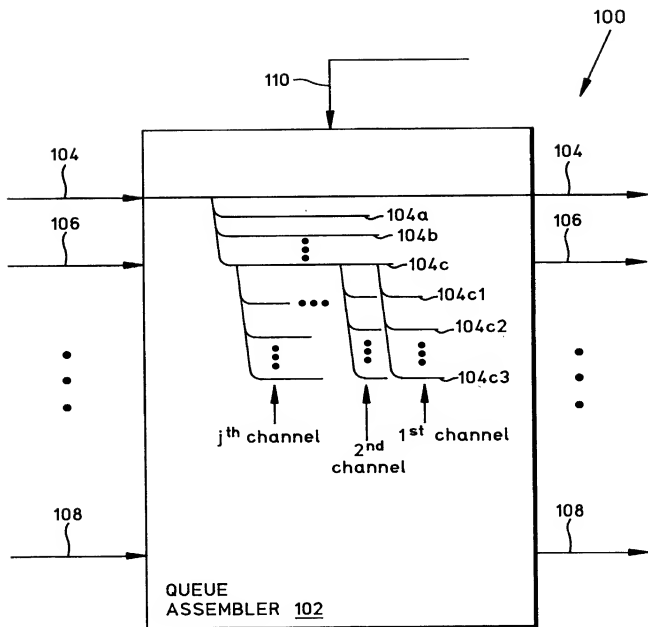


FIG. 2a

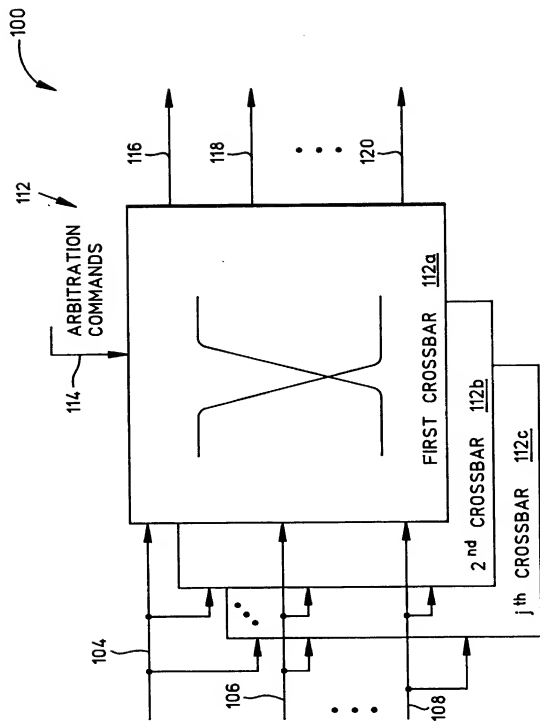
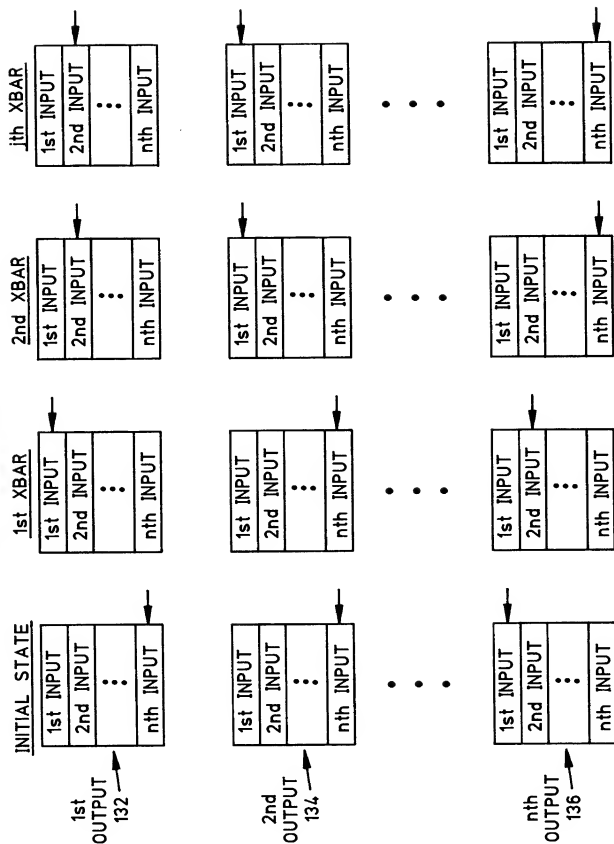


FIG. 2b



# 1ST ARBITRATION CYCLE



AVAILABLE SW INPUT PRIORITY LISTS

FIG. 3b

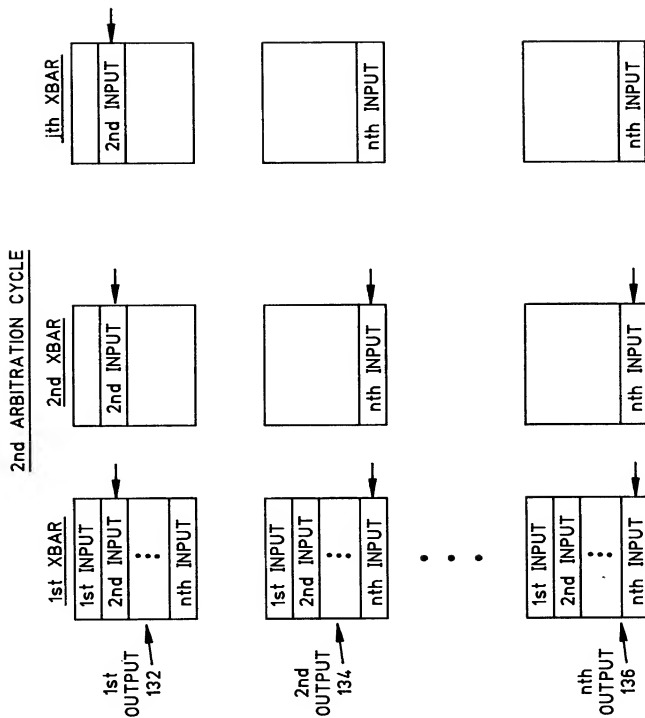


FIG. 3c

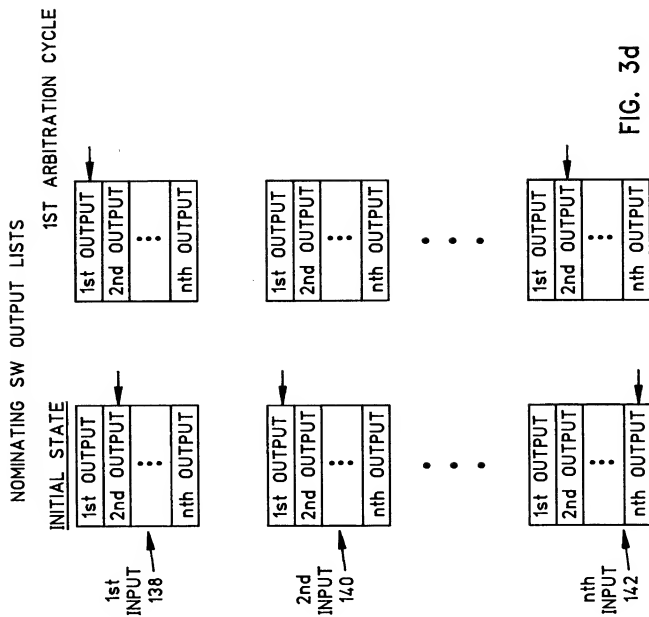
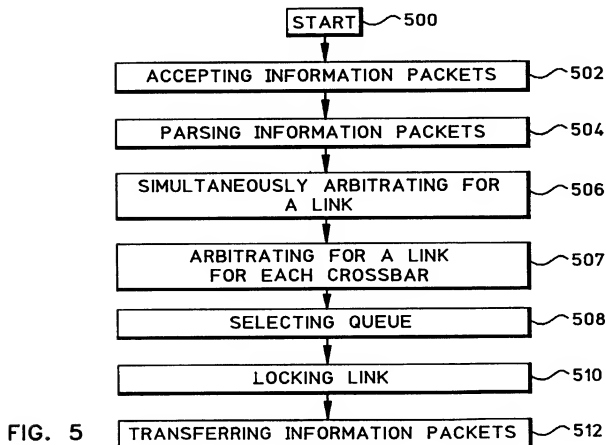
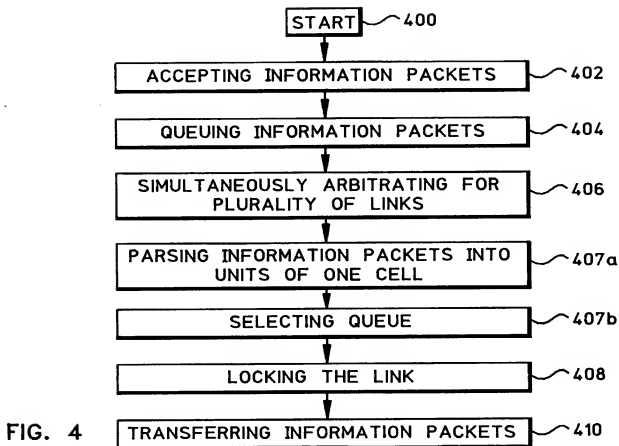


FIG. 3d





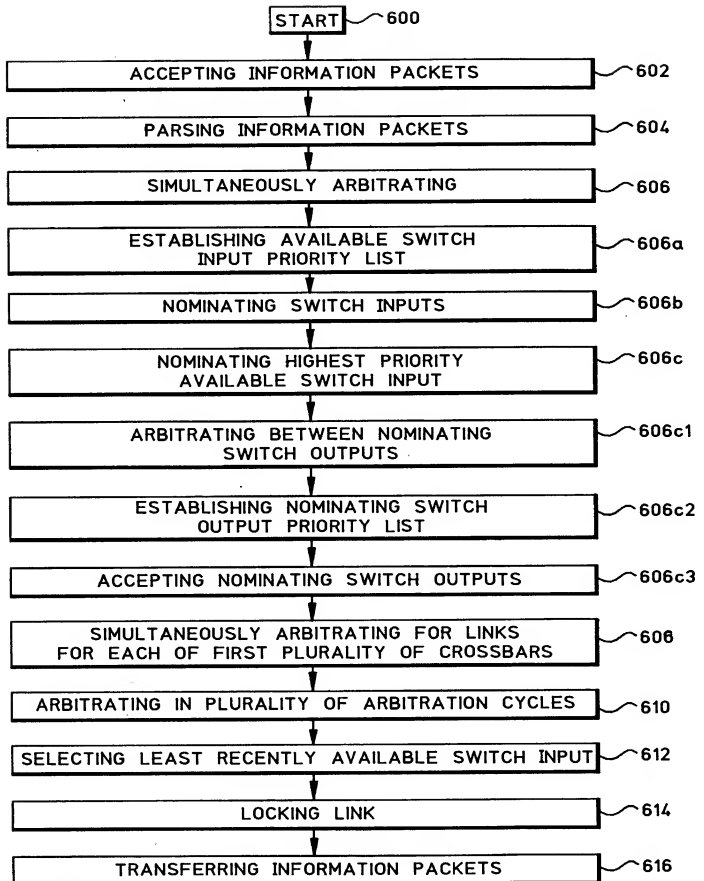


FIG. 6

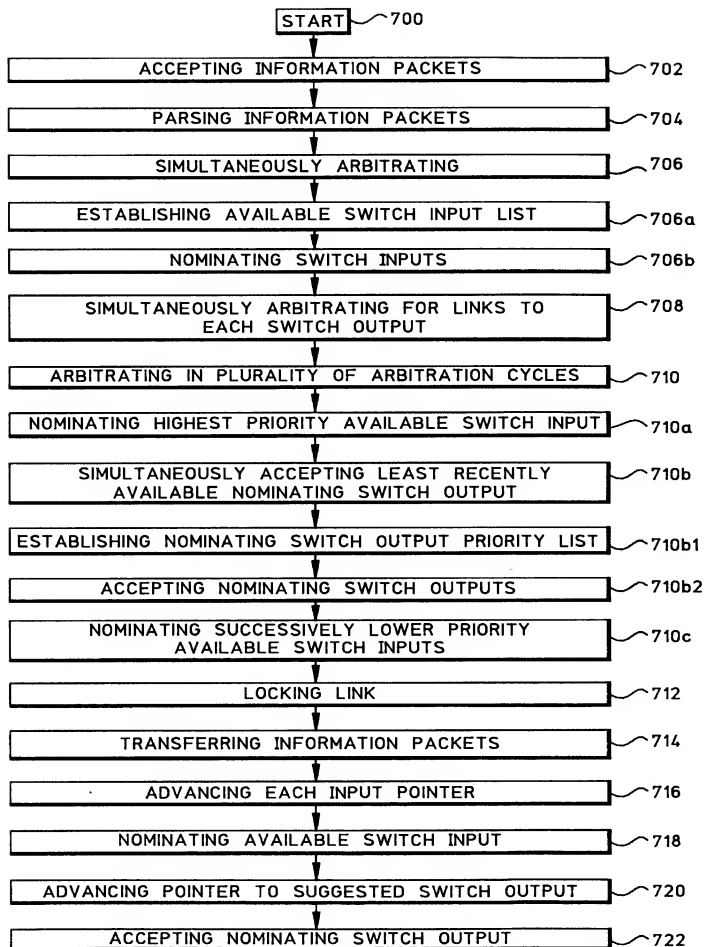


FIG. 7

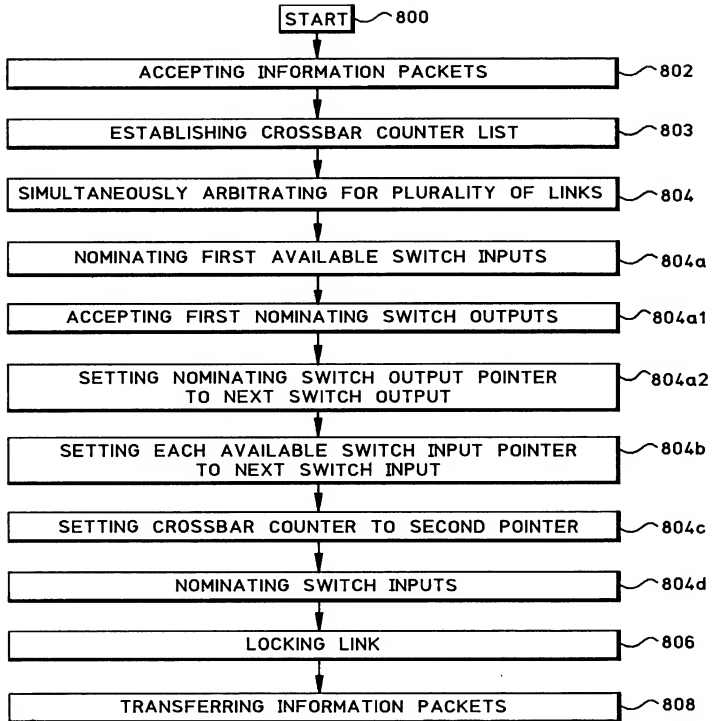


FIG. 8

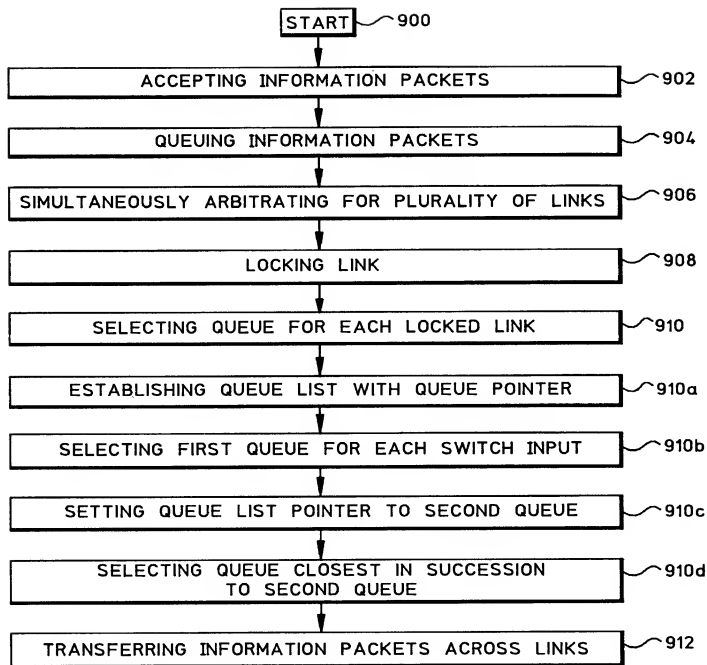


FIG. 9

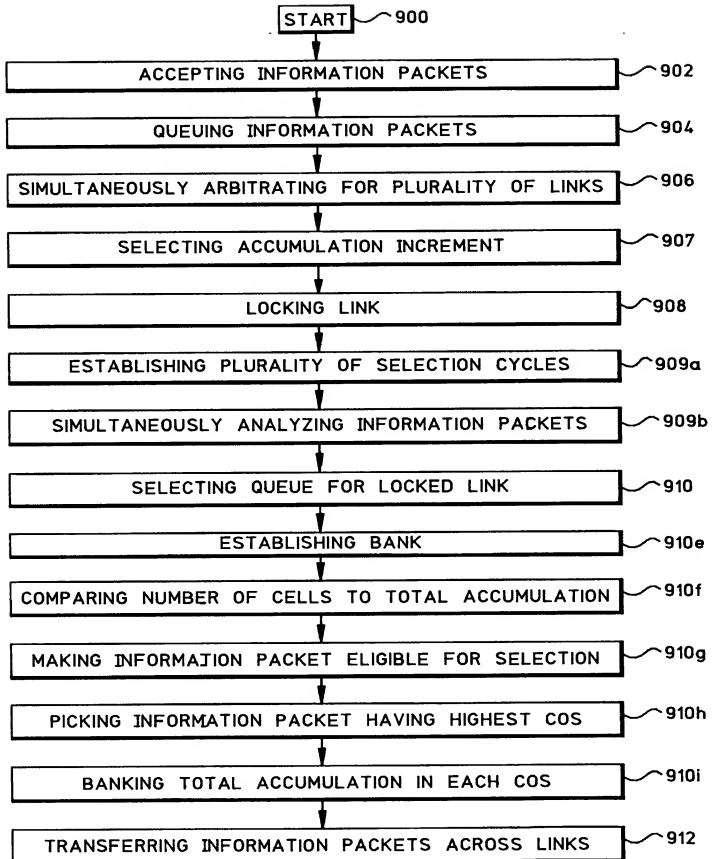


FIG. 10

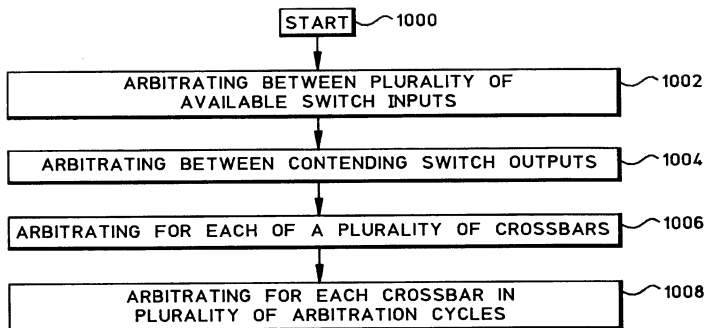


FIG. 11